

## ANNIE1-electronics-design - Task #8813

### Design for clock distribution and timing

05/15/2015 09:50 AM - Matthew Wetstein

<b>Status:</b>	Assigned	<b>Start date:</b>	06/01/2015
<b>Priority:</b>	Normal	<b>Due date:</b>	01/08/2016
<b>Assignee:</b>	Jonathan Eisch	<b>% Done:</b>	100%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>		<b>Spent time:</b>	0.00 hour
<b>Description</b>			

#### History

##### #1 - 06/10/2015 10:08 AM - Jonathan Eisch

- Tracker changed from Bug to Task

##### #2 - 09/16/2015 01:13 PM - Matthew Wetstein

- % Done changed from 0 to 40

##### #3 - 10/09/2015 01:14 PM - Jonathan Eisch

- Status changed from New to Assigned

- Assignee set to Jonathan Eisch

##### #4 - 12/17/2015 05:28 PM - Jonathan Eisch

- Due date changed from 06/30/2015 to 01/08/2016

##### #5 - 04/06/2016 11:14 PM - Matthew Wetstein

- % Done changed from 40 to 80

##### #6 - 01/25/2018 07:15 AM - Matthew Wetstein

- % Done changed from 80 to 100